

Substitute for Form 1449/PTO

INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(use as many sheets as necessary)

<i>Complete If Known</i>	
Application Number	
Filing Date	
First Named Inventor:	David Bistry
Art Unit	2183
Examiner Name	Daniel H. Pan

Sheet	1	of	10	Attorney Docket Number	42P2737C4
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U.S. PATENT DOCUMENTS

Examiner Initials*	Cite No. ¹	Document Number		Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind Code ² (if known)				
E.C.		us-	4,707,800	3/4/1985	Montrone, et al.	
		us-	4,989,168	11/21/1988	Kuroda, et al.	
		us-	3,723,715	8/25/1982	Chen, et al.	
		us-	4,498,177	8/30/1982	Larson	
		us-	3,711,692	3/15/1971	Batcher	
		us-	5,095,457	2/1/1990	Jeong	
		us-	4,393,468	3/26/1981	New	
		us-	4,418,383	6/30/1980	Doyle, et al.	
		us-	5,187,679	6/5/1991	Vassiliadis, et al.	
		us-	4,161,784	1/5/1978	Cushing, et al.	
		us-	4,771,379	10/21/1986	Ando, et al.	
		us-	5,634,118	4/10/1995	Blomgren	
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		us-	5,651,125	7/10/1995	Witt, et al.	
		us-	5,701,508	12/19/1995	Glew, et al.	
E.C.		us-	5,760,792	5/1/1995	Holt, et al.	

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		Country Code ³ Number ⁴ Kind Code ⁵ (if known)					
E.C.		WO93/01543		07/08/92	PCT		
		92106065.3		08/04/92	Europe		
		93120933.2		12/27/93	Europe		
		EP 96 94 4983		9/15/99	Supplemental Partial European Search Report		
E.C.		PCT/US96/20522		4/23/1997	International Search Report		

Examiner Signature	<i>Eun C</i>	Date Considered <i>6/21/07</i>	
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EC		us-	5,721,892	11/6/95	Peleg, et al.	
		us-	5,677,862	4/2/1996	Peleg, et al.	
		us-	5,467,473	1/8/1993	Kahle, et al.	
		us-	5,267,350	10/25/1990	Matsubara, et al.	
		us-	5,241,635	8/21/1989	Papadopoulos, et al.	
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		us-	5,535,397	6/30/1993	Durante, et al.	
		us-	5,481,719	9/9/1994	Ackerman, et al.	
		us-	5,008,812	3/18/1988	Bhandarkar, et al.	
		us-	5,546,554	2/2/1994	Yung, et al.	
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		us-	4,229,801	12/11/1978	Whipple	
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EC		PCT/US96/20516		4/18/1997	International Search Report		
		PCT/US96/20572		3/21/1997	International Search Report		
		PCT/US96/20573		3/11/1997	International Search Report		
		PCT/US96/20516		12/15/1997	Written Opinion		
EC		PCT/US96/20516		4/7/1998	International Preliminary Examination Report		

Examiner Signature

*Eric B.*Date Considered
6/21/07

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EC		us-	5,657,253	5/15/92	Dreyer, et al.	
		us-	5,835,748	12/19/1995	Orenstein, et al.	
		us-	5,852,726	12/19/1995	Lin, et al.	
		us-	5,857,096	12/19/1995	Bistry, et al.	
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		us-	5,978,901	11/2/1999	Luedtke et al.	
		us-	6,339,823	1/15/2002	Loper, Jr.	
		us-	6,412,065	6/25/2002	Loper, Jr.	
EC		us-	6,385,716	5/7/2002	Henry et al.	

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EC		PCT/US96/20572		10/17/1997	International Preliminary Examination Report		
		PCT/US96/20522		10/23/1997	Written Opinion		
		PCT/US96/20522		3/19/1998	International Preliminary Examination Report		
EC		PCT/US96/20573		11/19/1997	International Preliminary Examination Report		
Examiner Signature	<i>Eric Al</i>				Date Considered <i>6/21/07</i>		

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David Bistry

Art Unit

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Daniel H. Pan

Sheet	4	of	10	Attorney Docket Number	42P2737C4
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EC		us-	5,938,756	8/17/1999	Van Hook et al.	
		us-	5,487,159	1/23/1996	Byers et al.	
		us-	5,426,783	6/20/1995	Norrie et al.	
		us-	5,268,995	12/7/1993	Diefendorff et al.	
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		us-	5,669,013	9/97	Watanabe et al.	
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Examiner Signature

*Eric*Date Considered
6/21/07

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First Named Inventor: **David Bistry**

Art Unit **2183**

Examiner Name **Daniel H. Pan**

Sheet **5** of **10**

Attorney Docket Number **42P2737C4**

NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published	T ²
E.C.		J. Shipnes, <u>Graphics Processing with the 88110 RISC Microprocessor</u> , IEEE (1992), PP 169-174.	
		<u>MC88110 Second Generation RISC Microprocessor User's Manual</u> , Motorola Inc. (1991).	
		<u>Errata to MC88110 Second Generation RISC Microprocessor User's Manual</u> , Motorola Inc. (1992), pp 1-11.	
		<u>MC88110 Programmer's Reference Guide</u> , Motorola Inc. (1992), p 1-4.	
		<u>i860™ Microprocessor Family Programmer's Reference Manual</u> , Intel Corporation (1992), Ch. 1, 3, 8, 12.	
		R.B. Lee, <u>Accelerating Multimedia With Enhanced Microprocessors</u> , IEEE Micro (April 1995), pp 22-32	
		<u>TMS320C2x User's Guide</u> , Texas Instruments (1993) pp 3-2 through 3-11; 3-28 through 3-34; 4-1 through 4-22; 4-41; 4-103; 4-119 through 4-120; 4-122; 4-150 through 4-151.	
		L. Gwennap, <u>New PA-RISC Processor Decodes MPEG Video</u> , Microprocessor Report (Jan. 1994), pp 16, 17.	
		<u>SPARC Technology Business, UltraSPARC Multimedia Capabilities On-Chip Support for Real-Time Video and Advanced Graphics</u> , Sun Microsystems (Sept. 1994).	
		Y. Kawakami, et al., <u>LSI Applications: A Single-Chip Digital Signal Processor for Voiceband Applications</u> , Solid State Circuits Conference, Digest of Technical Papers; IEEE International (1980).	
		B. Case, <u>Phillips Hopes to Displace DSPs with VLIW</u> , Microprocessor Report (Dec. 1994), pp 12-15.	
		Rounce, P., et al.: <u>ARCHITECTURES WITHIN THE ESPRIT SPAN PROJECT</u> IEEE Micro.	
E.C.		<u>Graphics Floating Point Engine Fix and Float Registers Consolidations</u> , IBM Technical Disclosure Bulletin, Vol. 36, No. 12, dated December 1993.	
Examiner Signature	<i>Eric B</i>		Date Considered 6/21/07

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E.C.		L. Gwennap, <u>UltraSparc Adds Multimedia Instructions</u> , Microprocessor Report (Dec. 1994) pp 16-18.			
		N. Margulis, <u>i860 Microprocessor Architecture</u> , McGraw Hill, Inc. (1990), Ch. 6, 7, 8, 10, 11.			
		<u>Pentium Processor User's Manual, Volume 3; Architecture and Programming Manual</u> , Intel Corporation (1993), Ch. 1, 3, 4, 6, 8, and 18.			
		<u>i860™ Microprocessor Family Programmer's Reference Manual</u> , Intel Corporation (1992), Ch. 2, 9, 10, 11.			
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		Linley Gwennap, "UltraSparc Adds Multimedia Instructions, Other New Instructions Handle Unaligned and Little-Endian Data", Microprocessor Report, pgs. 16-18, December 5, 1994.			
		L. Kohn, et al., "The Visual Instruction Set (VIS) in UltraSPARC™", IEEE Computer Society Press, pgs. 462-469, Los Alamitos, California, 1995.			
		Philip Koopman, Jr., "Stack Computers the new wave", Mountain View Press, pgs. 3-234, La Honda, California, 1989.			
		Centaur's Reply to Intel's Counterclaims of May 16, 2002, In the United States District Court for the Western District of Texas, Austin Division, Civil Action No. A-01-CA-602-SS, <u>Via Technologies, Inc., a Taiwan Corporation, and Centaur Technology, Inc. vs. Intel Corporation vs. Via-Cyrix, Inc., a Texas Corporation, and Via Technologies, Inc. (U.S.)</u> , filed May 31, 2002 (12 pgs.)			
E.C.		Reply of Intel to Centaur's Counterclaim, In the United States District Court for the Western District of Texas, Austin Division, Civil Action No. A-01-CA-602-SS, <u>Via Technologies, Inc., a Taiwan Corporation, and Centaur Technology, Inc. vs. Intel Corporation vs. Via-Cyrix, Inc., a Texas Corporation, and Via Technologies, Inc. (U.S.)</u> , filed June 13, 2002 (4 pgs)			
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EC		Defendants' Particulars of Objections, In the High Court of the Hong Kong Special Administrative Region (Hong Kong), Court of First Instance of NO. 4265 OF 2001, Case No. HCA 4265/2001, <u>Intel Corporation vs. Via Technologies Inc.; Trend Electronics (HK) Limited</u> , dated December 27, 2001 (4 pgs)				
		Defence and Counterclaim, In the High Court of Justice Chancery Division Patents Court (United Kingdom), Case No. HC 01 No. C04136, <u>Intel Corporation vs. Via Technologies Inc. (a company incorporated in Taiwan), Via Technologies (Europe) Limited, Realtime Distribution Limited</u> , dated December 12, 2001 (pgs. 1-13); Annex 1 to the Defence and Counterclaim (2 pgs); Annex 2 to the Defence and Counterclaim (4 pgs.); Cover page (1 pg)				
		Defendants' Particulars of Objections, In the High Court of Justice Chancery Division Patents Court (United Kingdom), Case No. HC 01 No. C04136, <u>Intel Corporation vs. Via Technologies Inc. (a company incorporated in Taiwan), Via Technologies (Europe) Limited, Realtime Distribution Limited</u> , dated December 12, 2001 (4 pgs)				
		<u>VIA Technologies Inc., a Taiwan Corporation, and Centaur Technology, Inc., a California Corporation, v. Intel Corporation, a Delaware Corporation, "VIA's and Centaur's Short Statement Regarding Their Non-Infringement and Invalidity Defenses -Filed Under Seal (Redacted),"</u> In the United States District Court for the Western District of Texas, Austin Division, Civil Action No. A-01-CA-602-SS, filed January 24, 2002; total pages: 14.				
EC		<u>VIA Technologies Inc., and Centaur Technology, Inc., v. Intel Corporation v. VIA-Cyrix, Inc. and VIA Technologies, Inc. (US), "VIA Parties' Second Supplemental Responses to Intel's Interrogatory (No. 3) and Third Supplemental Response to Intel's Interrogatory (No. 6) (Redacted),"</u> In the United States District Court for the Western District of Texas, Austin Division, Civil Action No. A-01-CA-602-SS (Consolidated with Civil Action No. A-02-CA-420-SS), dated April 2, 2003; total pages: 46.				
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Substitute for Form 1449/PTO

INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(use as many sheets as necessary)

Complete If Known

Application Number	
Filing Date	
First Named Inventor:	David Bistry
Art Unit	2183
Examiner Name	Daniel H. Pan

Sheet 8 of 10 Attorney Docket Number 42P2737C4

NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published	T ²
{C,		<u>VIA Technologies Inc., and Centaur Technology, Inc., v. Intel Corporation v. VIA-Cyrix, Inc.</u> , and <u>VIA Technologies, Inc. (US)</u> , "VIA Parties' Notice Under 35 U.S.C. § 282," In the United States District Court for the Western District of Texas, Austin Division, Civil Action No. A-01-CA-602-SS, dated March 7, 2003; total pages: 21.	
		<u>VIA Technologies Inc., and Centaur Technology, Inc., v. Intel Corporation v. VIA-Cyrix, Inc.</u> , <u>VIA Technologies, Inc. (US)</u> , and IP First, L.L.C., "VIA Parties' Motion For Summary Judgment That U.S. Patent No. 5,835,748 Is Invalid For Failure To Disclose The Best Mode – Filed Under Seal," In the United States District Court for the Western District of Texas, Austin Division, Civil Action No. A-01-CA-602-SS (Consolidated with Civil Action No. A-02-CA-420-SS), dated March 17, 2003; total pages: 106 (including tabs)	
		<u>VIA Technologies Inc., and Centaur Technology, Inc., v. Intel Corporation v. VIA-Cyrix, Inc.</u> , <u>VIA Technologies, Inc. (US)</u> , and IP First, L.L.C., "Intel's Opposition To The VIA Parties' Motion for Summary Judgment of Invalidity of U.S. Patent No. 5,835,748 Based On Best Mode," In the United States District Court for the Western District of Texas, Austin Division, Civil Action No. A-01-CA-602-SS (Consolidated with Civil Action No. A-02-CA-420-SS), filed March 24, 2003; total pages: 31 (including tabs).	
		<u>VIA Technologies Inc., a Taiwan Corporation, and Centaur Technology, Inc., a California Corporation, v. Intel Corporation, a Delaware Corporation</u> , "The VIA Parties' Reply To Intel's Opposition To The VIA Parties' Motion for Summary Judgment That U.S. Patent No. 5,835,748 Is Invalid For Failure To Disclose The Best Mode – Filed Under Seal," In the United States District Court for the Western District of Texas, Austin Division, Civil Action No. A-01-CA-602-SS, dated March 27, 2003; total pages: 44 (including tabs).	
		<u>VIA Technologies Inc., and Centaur Technology, Inc., v. Intel Corporation</u> , "ORDER: That...VIA Parties' Motion For Summary Judgment That U.S. Patent No. 5,835,748 Is Invalid For Failure To Disclose The Best Mode [#356] is DENIED..., signed by Sam Sparks, United States District Judge," In the United States District Court for the Western District of Texas, Austin Division, Case No. A-01-CA-602-SS, dated March 31, 2003; total pages: 23.	
{C,		<u>VIA Technologies Inc., and Centaur Technology, Inc., v. Intel Corporation v. VIA-Cyrix, Inc.</u> , <u>VIA Technologies, Inc. (US)</u> , and IP First, L.L.C., "Expert Report of Harold Stone (2)," In the United States District Court for the Western District of Texas, Austin Division, Civil Action No. A-01-CA-602-SS (Consolidated with Civil Action No. A-02-CA-420-SS), dated March 17, 2003; total pages: 89 (including tabs).	

Examiner Signature

Date Considered

6/21/07

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INFORMATION DISCLOSURE STATEMENT BY APPLICANT

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Complete If Known

Sheet	9	of	10	Attorney Docket Number	42P2737C4
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NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published	T ²
E.C.		VIA Technologies Inc., and Centaur Technology, Inc., v. Intel Corporation v. VIA-Cyrix, Inc., VIA Technologies, Inc. (US), and IP First, L.L.C., "Rebuttal Expert Report of Dr. V. Thomas Rhyne Regarding The Validity of U.S. Patent Nos. 5,701,508; 5,835,748 and 5,201,043," In the United States District Court for the Western District of Texas, Austin Division, Civil Action No. A-01-CA-602-SS (Consolidated with Civil Action No. A-02-CA-420-SS), dated March 24, 2003; total pages: 73 (including tab).	
		Intel Corporation (a company incorporated in the state of Delaware USA) vs. (1) VIA Technologies Inc. (a company incorporated in Taiwan), (2) VIA Technologies (Europe) Limited, (3) Realtime Distribution Limited, "Intel's Opening Skeleton Argument (Redacted)," In the High Court of Justice Chancery Division Patents Court (United Kingdom), Case No. HC 01 No. C04136, dated 25 February 2003; total pages: 100.	
		Intel Corporation (a company incorporated in the state of Delaware USA) vs. (1) VIA Technologies Inc. (a company incorporated in Taiwan), (2) VIA Technologies (Europe) Limited, (3) Realtime Distribution Limited, "Intel's Closing Skeleton Argument (Redacted)," In the High Court of Justice Chancery Division Patents Court (United Kingdom), Case No. HC 01 No. C04136, dated 13 th February 2003; total pages: 144.	
		Intel Corporation vs. VIA Technologies Inc. (a company incorporated in Taiwan), Via Technologies (Europe) Limited, Realtime Distribution Limited, "Intel Corporation v. VIA Technologies, Inc., and others; Defendants' Opening Skeleton Argument – Introduction to the Proceedings (Redacted)," In the High Court of Justice Chancery Division Patents Court (United Kingdom), Case No. HC 01 No. C04136, dated 27 th January 2003; total pages: 99 (including tabs).	
		Intel Corporation vs. VIA Technologies Inc. (a company incorporated in Taiwan), Via Technologies (Europe) Limited, Realtime Distribution Limited, "Intel Corporation v. VIA Technologies, Inc., and others; Defendants' Closing Skeleton Argument (Redacted)," In the High Court of Justice Chancery Division Patents Court (United Kingdom), Case No. HC 01 No. C04136, dated 13 th February 2003; total pages: 70 (including tabs).	
E.C.		Intel Corporation (a company incorporated in the state of Delaware USA) vs. (1) VIA Technologies Inc. (a company incorporated in Taiwan), (2) VIA Technologies (Europe) Limited, (3) Realtime Distribution Limited, "Expert's Report of Simon Richard Jones (Redacted)," In the High Court of Justice Chancery Division Patents Court (United Kingdom), Case No. HC 01 No. C04136, dated 20/12/02 (December 20, 2002); total pages: 109 (including tabs).	
Examiner Signature			Date Considered 6/2/03

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Substitute for Form 1449/PTO INFORMATION DISCLOSURE STATEMENT BY APPLICANT <i>(use as many sheets as necessary)</i>				Complete If Known		
				Application Number		
				Filing Date		
				First Named Inventor:	David Bistry	
				Art Unit	2183	
				Examiner Name	Daniel H. Pan	
Sheet	10	of	10	Attorney Docket Number	42P2737C4	
NON PATENT LITERATURE DOCUMENTS						
Examiner Initials*	Cite No ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published				T ²
E.C.		<u>Intel Corporation</u> (a company incorporated in the state of Delaware USA) vs. (1) <u>VIA Technologies Inc.</u> (a company incorporated in Taiwan), (2) <u>VIA Technologies (Europe) Limited</u> , (3) <u>Realtime Distribution Limited</u> , "Expert Report of Michael David May (Redacted)," In the High Court of Justice Chancery Division Patents Court (United Kingdom), Case No. HC 01 No. C04136, dated 23 December 2002; total pages: 268 (including tabs).				
		<u>Intel Corporation</u> (a company incorporated in the state of Delaware USA) vs. (1) <u>VIA Technologies Inc.</u> (a company incorporated in Taiwan), (2) <u>VIA Technologies (Europe) Limited</u> , (3) <u>Realtime Distribution Limited</u> , "Expert Report of Michael David May: Exhibits MDM-1 to MDM-11 (Redacted)," In the High Court of Justice Chancery Division Patents Court (United Kingdom), Case No. HC 01 No. C04136, dated 23 December 2002; total pages: 367 (including tabs).				
		<u>Intel Corporation</u> (a company incorporated in the state of Delaware USA) vs. (1) <u>VIA Technologies Inc.</u> (a company incorporated in Taiwan), (2) <u>VIA Technologies (Europe) Limited</u> , (3) <u>Realtime Distribution Limited</u> , "Bundle X – Exhibits Produced At Trial," In the High Court of Justice Chancery Division Patents Court (United Kingdom), Case No. HC 01 No. C04136, dates produced: 05.02.03 – 11.02.03; total pages: 139 (including tabs).				
E.C.		<u>Intel Corporation</u> (a company incorporated in the state of Delaware USA) vs. (1) <u>VIA Technologies Inc.</u> (a company incorporated in Taiwan), (2) <u>VIA Technologies (Europe) Limited</u> , (3) <u>Realtime Distribution Limited</u> , "Order Before the Honorable Mr. Justice Pumfrey, Monday the 3 rd day of February 2003: Claimant has leave to amend the Specification of United Kingdom Patent No. GB 2 326 494...", In the High Court of Justice Chancery Division Patents Court (United Kingdom), Case No. HC 01 No. C04136, dated 3 rd Day of February 2003; total pages: 2.				
Examiner Signature	<i>Eric El</i>			Date Considered	6/21/07	

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FOREIGN PATENT DOCUMENTS						
Examiner Initials*	Cite No. ¹	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T ²
		Country Code ³ Number ⁴ Kind Code ⁵ (if known)				
CC		WO93/01543	1/21/1993	PCT patent application		
CC		JP58080740	5/14/1983	Watanabe Masanobu		
CC		JP58080740 (English Translation)	5/14/1983	Watanabe Masanobu		
Examiner Signature	<i>EE A</i>				Date Considered	6/21/07

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INFORMATION DISCLOSURE

STATEMENT BY APPLICANT

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Sheet		1	of	Complete If Known	
				Application Number	10/805,609
				Filing Date	03/19/2004
				First Named Inventor:	David Bistry et al.
				Art Unit	2122
				Examiner Name	Not Yet Assigned
				Attorney Docket Number	042390.P2737C4

U.S. PATENT DOCUMENTS

Examiner Initials*	Cite No. ¹	Document Number		Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind Code ² (if known)				
EC	US-	6,598,149	B1	07-22-2003	Clift	
	US-	6,266,686	B1	07-24-2001	Bistry et al.	
	US-	5,852,726	A	12-22-1998	Lin et al.	
	US-	6,425,074	B1	07-23-2002	Meier et al.	
	US-	6,405,305	B1	06-11-2002	Meier et al.	
	US-	6,247,106	B1	06-12-2001	Witt	
	US-	5,968,169	A	10-19-1999	Pickett	
EC	US-	5,761,105		06-02-1998	Goddard et al.	
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Examiner Initials*	Cite No. ¹	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T ⁶
		Country Code ³ Number ⁴ Kind Code ⁵ (if known)				

Examiner Signature

Eric Al

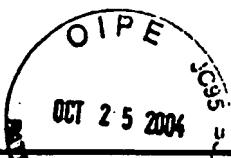
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6/21/07

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**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**

(use as many streets as necessary)

Sheet 1 of

Substitute for form 1449 ATO				Complete If Known	
INFORMATION DISCLOSURE STATEMENT BY APPLICANT <i>(use as many sheets as necessary)</i>				Application Number	10/805,609
				Filing Date	March 19, 2004
				First Named Inventor	David Bistry
				Art Unit	2183
				Examiner Name	Daniel H. Pan
				Attorney Docket Number	42P2737C4
Sheet	1	of	1		

U.S. PATENT DOCUMENTS

FOREIGN PATENT DOCUMENTS

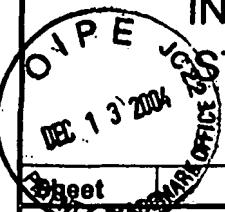
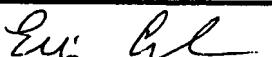
Examiner Signature		Date Considered	6/21/07
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Based on PTO/SB/08A (08-03) as modified by Blakely, Solakoff, Taylor & Zafman (w/r) 08/11/2003.

Send To: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450

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 Sheet 1 of 1		Application Number 10/805,609					
		Filing Date 03-19-2004					
		First Named Inventor: David Bistry					
		Art Unit 2122					
		Examiner Name Not yet assigned					
				Attorney Docket Number 42P2737C4			
U.S. PATENT DOCUMENTS							
Examiner Initials*	Cite No. ¹	Document Number		Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	
		Number-Kind Code ² (if known)					
CC.	US-	5,794,060	08/11/1998	Hansen, et al.			
	US-	5,809,321	09/15/1998	Hansen, et al.			
	US-	5,953,241	09/14/1999	Hansen, et al.			
	US-	5,778,419	07/07/1998	Hansen, et al.			
	US-	5,794,061	08/11/1998	Hansen, et al.			
	US-	5,822,603	10/13/1998	Hansen, et al.			
	US-	6,006,318	12/21/1999	Hansen, et al.			
	US-	6,295,599	09/25/2001	Hansen, et al.			
	US-	6,584,482	06/24/2003	Hansen, et al.			
	US-	6,378,060	04/23/2002	Hansen, et al.			
	US-	6,725,356	04/20/2004	Hansen, et al.			
	US-	6,643,765	11/04/2003	Hansen, et al.			
FOREIGN PATENT DOCUMENTS							
Examiner Initials*	Cite No. ¹	Foreign Patent Document		Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T ³
		Country Code ³ Number ⁴ Kind Code ⁵ (if known)					
Examiner Signature					Date Considered		6/21/07

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